



U1 MKL25Z128VLK4

1	PTE0/UART1_TX/RTC_CLKOUT/CMP0_OUT/I2C1_SDA
2	PTE1/SPI1_MOSI/UART1_RX/SPI1_MISO/I2C1_SCL
3	PTE2/SPI1_SCK
4	PTE3/SPI1_MISO/SPI1_MOSI
5	PTE4/SPI1_PCS0
6	PTE5
13	PTE20/ADC0_DP0/ADC0_SE0/TPM1_CH0/UART0_TX
14	PTE21/ADC0_DM0/ADC0_SE4A/TPM1_CH1/UART0_RX
15	PTE22/ADC0_DP3/ADC0_SE3/TPM2_CH0/UART2_TX
16	PTE23/ADC0_DM3/ADC0_SE7A/TPM2_CH1/UART2_RX
24	PTE24/TPM0_CH0/I2C0_SCL
25	PTE25/TPM0_CH1/I2C0_SDA
22	PTE30/DAC0_OUT/ADC0_SE23/CMP0_IN4/TPM0_CH3/TPM_CLKIN1
23	PTE31/TPM0_CH4
12	VREGIN
11	VOUT33
10	USB0_DM
9	USB0_DP
21	PTE29/CMP0_IN5/ADC0_SE4B/TPM0_CH2/TPM_CLKIN0
26	PTA0/TSI0_CH1/TPM0_CH5/SWD_CLK
27	PTA1/TSI0_CH2/UART0_RX/TPM2_CH0
28	PTA2/TSI0_CH3/UART0_TX/TPM2_CH1
29	PTA3/TSI0_CH4/I2C1_SCL/TPM0_CH0/SWD_DIO
30	PTA4/TSI0_CH5/I2C1_SDA/TPM0_CH1/NMI
31	PTA5/USB_CLKIN/TPM0_CH2
32	PTA12/TPM1_CH0
33	PTA13/TPM1_CH1
34	PTA14/SPI0_PCS0/UART0_TX
35	PTA15/SPI0_SCK/UART0_RX
36	PTA16/SPI0_MOSI/SPI0_MISO
37	PTA17/SPI0_MISO/SPI0_MOSI
40	PTA18/EXTAL0/UART1_RX/TPM_CLKIN0
41	PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1
42	PTA20/RESET
43	PTB0/LLWU_P5/ADC0_SE8/TSI0_CH0/I2C0_SCL/TPM1_CH0
44	PTB1/ADC0_SE9/TSI0_CH6/I2C0_SDA/TPM1_CH1
45	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/TPM2_CH0
46	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/TPM2_CH1
47	PTB8/EXTRG_IN
48	PTB9
49	PTB10/SPI1_PCS0
50	PTB11/SPI1_SCK
51	PTB16/TSI0_CH9/SPI1_MOSI/UART0_RX/TPM_CLKIN0/SPI1_MISO
52	PTB17/TSI0_CH10/SPI1_MISO/UART0_TX/TPM_CLKIN1/SPI1_MOSI
53	PTB18/TSI0_CH11/TPM2_CH0
54	PTB19/TSI0_CH12/TPM2_CH1
55	PTC0/ADC0_SE14/TSI0_CH13/EXTRG_IN/CMP0_OUT
56	PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/TSI0_CH14/I2C1_SCL/TPM0_CH0
57	PTC2/ADC0_SE11/TSI0_CH15/I2C1_SDA/TPM0_CH1
58	PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT
61	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3
62	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT
63	PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO
64	PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI
65	PTC8/CMP0_IN2/I2C0_SCL/TPM0_CH4
66	PTC9/CMP0_IN3/I2C0_SDA/TPM0_CH5
67	PTC10/I2C1_SCL
68	PTC11/I2C1_SDA
69	PTC12/TPM_CLKIN0
70	PTC13/TPM_CLKIN1
71	PTC16
72	PTC17
73	PTD0/SPI0_PCS0/TPM0_CH0
74	PTD1/ADC0_SE5B/SPI0_SCK/TPM0_CH1
75	PTD2/SPI0_MOSI/UART2_RX/TPM0_CH2/SPI0_MISO
76	PTD3/SPI0_MISO/UART2_TX/TPM0_CH3/SPI0_MOSI
77	PTD4/LLWU_P14/SPI1_PCS0/UART2_RX/TPM0_CH4
78	PTD5/ADC0_SE6B/SPI1_SCK/UART2_TX/TPM0_CH5
79	PTD6/LLWU_P15/ADC0_SE7B/SPI1_MOSI/UART0_RX/SPI1_MISO
80	PTD7/SPI1_MISO/UART0_TX/SPI1_MOSI

PROJECT:		REVISION
DRAWING TITLE:		
DRAWN BY:		
SHEET:	OF	
DATE:		(c) 2014 © King Kobo / dirthighway.net