



Pinout table for the STM32F411RET microcontroller (U5):

PA0	14	PA0	26	PB0	26
PA1	15	PA1	27	PB1	27
PA2	16	PA2	28	PB2	28
PA3	17	PA3	55	PB3	55
PA4	20	PA4	56	PB4	56
PA5	21	PA5	57	PB5	57
PA6	22	PA6	58	PB6	58
PA7	23	PA7	59	PB7	59
PA8	41	PA8	61	PB8	61
PA9	42	PA9	62	PB9	62
PA10	43	PA10	29	PB10	29
PA11	44	PA11	30	VCAP1	30
PA12	45	PA12	33	PB12	33
PA13	46	PA13	34	PB13	34
PA14	49	PA14	35	PB14	35
PA15	50	PA15	36	PB15	36
PC0	8	PC0	39	PC8	39
PC1	9	PC1	40	PC9	40
PC2	10	PC2	51	PC10	51
PC3	11	PC3	52	PC11	52
PC4	24	PC4	53	PC12	53
PC5	25	PC5	2	PC13	2
PC6	37	PC6	3	PC14	3
PC7	38	PC7	4	PC15	4
PH0	5	PH0-OSC_IN	7	NRST	7
PH1	6	PH1-OSC_OUT	60	BOOT0	60
VBAT	1	VBAT+	12		
VREF+	13	VREF+	54	PD2	54
VDD	64	VDD	31	VSSA	31
VDD	48	VDD	63	VSS	63
VDD	19	VDD	47	VSS	47
			18	VSS	18